

PATENT J. W. L. 2-10-05

OFFICE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Atty. Dkt. No: 568
Application No: 0

5681-52900 09/995,226

Inventor(s):

Patrick P. Hicks and Gary L. Gilbert

Title:

A Connector For Electrically Coupling One or More Devices In a Processor-Based System

Examiner: Abrams, Neil Group/Art Unit: 2839

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Washington, DC 20231, on the date indicated below.

B. Noël Kivlin

Name of Registered Representative

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RESPONSE TO OFFICE ACTION OF October 18, 2002

Commissioner for Patents Washington, D.C. 20231

Dear Sir:

This paper is submitted in response to the Office Action of October 18, 2002. Applicants also respectfully Request Continued Examination.

IN THE CLAIMS:

Please cancel claims 3, 9, and 12. Please add claims 30-35 and amend claims 1, 2, 4, 5, 8, 10, 18, 19, 20, 22 and 26 as follows:

- 1. (Twice amended) A processor-based system, comprising:
 - a first board;
 - a second board; and
 - a first connector coupled to the first board, comprising:

